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Applicant, Title of invention, Abstract — e.g. computer semiconductor

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piezoelectric

AND ▼

AND

substrate

AND ▼

AND

seed layer

AND ▼

AND

Date of publication of application — e.g. 19980401 - 19980405

AND

IPC — e.g. D01B7/04 A01C11/02

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bulk acoustic resonator

AND

AND

substrate

AND

AND

AND

AND

Date of publication of application — e.g. 19980401 - 19980405

AND

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	Type	L #	Hits	Search Text	DBs	Time Stamp	C mments	Error Definition
1	BRS	L1	49	piezoelectric and substrate same seed adj1 layer	USPAT	2004/05/27 15:00		
2	BRS	L2	49	piezoelectric and substrate same seed adj1 layer	US-PGP UB; EPO; JPO; DERWE NT; IBM_TD	2004/05/27 15:00		